

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named  
Inventor : Alexander E. Andreev et al.

Appln. No. :

Filed : HEREWITH

For : MEMORY MAPPING FOR PARALLEL  
TURBO DECODING

Docket No.: 03-0933/L13.12-0240

Group Art Unit:

Examiner:

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

EXPRESS MAIL NO. EV178016346US  
DATE OF DEPOSIT: August 26, 2003

The patents or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 C.F.R. § 1.97. Copies of the patents or publications cited are enclosed.

The Director is authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 23-1123.

Respectfully submitted,

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FORM PTO-1449	Atty. Docket No.: 03-0933/L13.12-0240	Appl. No.:
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Alexander E. Andreev et al.	
	Filing Date	Group Art:
	HEREWITH	

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
AA	10/021,414		Eneboe et al.			10/30/2001
AB	10/299,270		Andreev et al.			11/19/2002
AC	4,656,592	4/1987	Spaananburg et al.	364	490	
AD	5,752,070	5/1998	Martin et al.	395	800.33	
AE	5,898,677	4/1999	Deeley et al.	370	276	
AF	6,152,613	11/2000	Martin et al.	395	800.33	
AG						
AH						
AI						
AJ						
AK						

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
AL						
AM						
AN						

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	"On-chip Networks weighed as wiring alternative," by R. Wilson, Integrated System Design, June 25, 2001, URL: <a href="http://www.eet.com/OEG20010625S0099">http://www.eet.com/OEG20010625S0099</a>
AP	"Configurable Computing," by J. Villasenor et al.; Scientific American, June 1997, URL: <a href="http://sciam.com/0697issue/0697villasenor.html">http://sciam.com/0697issue/0697villasenor.html</a>
AQ	"Models of Computation, Exploring the Power of Computing" by John E. Savage, pp. 309-311, 1998

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.